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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q56456

Claire BESSET-BATHIAS

Appln. No.: 09/429,028

Group Art Unit: 2664

Confirmation No.: 5444

Examiner: Chirag G. SHAH

Filed: October 29, 1999

For: A METHOD FOR GENERATING ATM CELLS FOR LOW BIT RATE APPLICATIONS

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

RECEIVED

OCT 31 2002

Technology Center 2600

Sir:

In response to the Office Action dated July 30, 2002, please amend the above-identified application as follows:

IN THE SPECIFICATION:

The specification is changed as follows:

Page 7, fourth full paragraph:

The second embodiment of the present invention, illustrated in figure 2, corresponds to the case where said intra priority multiplexing is carried out at ATM Adaptation Layer (AAL) level, and said inter-priority multiplexing is carried out at ATM Layer level.